ABSTRACT OF THE DISCLOSURE

A hybrid method of reprogramming the memory of an electronic module initializes the module and prepares it for the capability to subsequently receive information via high speed wireless transmission by initially down-loading to the module memory via a relatively low speed wire bus, a boot loader program and an operating system kernel. Subsequently, the remaining module memory is reprogrammed, or flashed by wirelessly transmitting new memory information in accordance with wireless protocol information passed to the module by the wire bus.